#epd

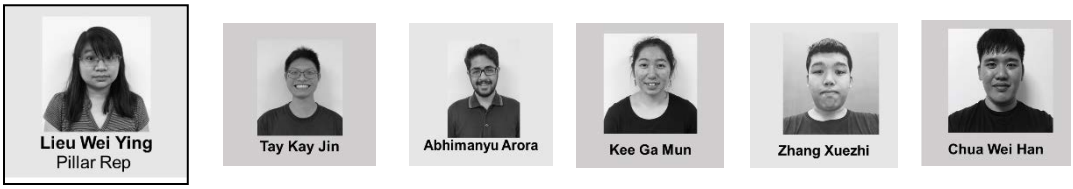
OCT 2018

MESSAGE FROM HOP, PROF RICKY ANG

The end of term is drawing close and deadlines are looming. But in the midst of this all, there is some time to take a short breather. Wishing all our Hindu friends a Happy Deepavali next week, from all of us here at EPD.

ANNOUNCEMENTS

- Let's welcome the fifth Node for the undergraduate students. The new Pillar Rep is **Lieu Wei Ying**. Feel free to reach out to them if you are interested in organising or participating in any events, or have any feedback for the Pillar.



- 30.113 Design and Fabrication of Microelectromechanical Systems will not be offered in Term 7, 2019.

FEATURED: EPD SITE VISIT TO INTEL

WRITTEN BY: FANG WEN HAO, DERENCE (SOPHOMORE, MECHANICAL ENGINEERING TRACK)

When I found out that the Pillar was organizing a site visit to Intel Singapore, I jumped at the chance because I wanted to learn more about how the theories we learn at EPD can be put to practical use. This site visit to the world's second largest chip maker and the top semiconductor R&D spender would also be a great exposure to the engineering industry.

We were first introduced to Intel's operations and work by Mr. Philippe Royannez, the Managing Director for Intel CDG Southeast Asia. He shared many interesting facts regarding the evolution of both Intel and microchip technology (Fun fact: it is so advanced now that up to a trillion transistors can be placed in a chip!). The next presentation by Mr. Frederic Goffin, Senior Architect GNSS products, allowed us to understand more about GPS and positioning technologies, such as the different factors for the measurement of location by satellites and parameters for a GPS receiver.

The highlight of the visit for me was definitely the tour of the Intel lab which was highly secured and restricted to outsiders. From the outside, it looks just like an ordinary lab but the Lab Manager, Mr Sai Kin, introduced us to the very rigorous process of building microchips. He explained the importance of every step during testing to ensure the functionality of the microchip prototype, including the ability to detect weak signals amongst the surrounding noise, as well as working under harsh environmental conditions.

Personally, the most important lesson that I've taken away is that we must constantly look towards the future for greater developments, like how Intel has progressed from producing memory chips to processors, and finally aims to be data-centric in the near future. We ended the site visit with a great opportunity to mingle with the company's interns and staff. As we enjoyed the ice cream that Intel Singapore kindly prepared for us, we learned more about the work experience in Intel and future opportunities available for us undergraduates. If you have an interest in integrated circuits or microchips, this site visit is surely worth your trip!

The view from the office

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ENGINEERING PRODUCT DEVELOPMENT